

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lap-Wai Chow, et al.) Group: Not yet assigned
 Serial No.: Not yet assigned) Examiner: Not yet assigned
 Filed: Concurrently herewith) Our Ref: B-4505NP 620843-6
 For: "USE OF SILICON BLOCK)
 PROCESS STEP TO CAMOUFLAGE A)
 FALSE TRANSISTOR") Date: August 7, 2003

CLAIM TO PRIORITY UNDER 35 U.S.C. 119

Mail Stop Patent Application
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

[X] Applicants hereby make a right of priority claim under 35
 U.S.C. 119(e) for the benefit of the filing date(s) of the
 following corresponding provisional application(s)

<u>COUNTRY</u>	<u>FILING DATE</u>	<u>PROVISIONAL APPLICATION NO.</u>
U.S.A.	22 November 2002	60/428,634

Respectfully submitted,



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